IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Luetzen, et al. Docket No.: INF 2004 SP 00115 US

Serial No.: 10/721,225 Art Unit: 1792

Filed: November 26, 2003 Examiner: George A. Goudreau

For: Method and Structures for Increasing the Structure Density and the Storage

Capacitance in a Semiconductor Wafer

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Submission of Translation of Priority Document

Dear Sir:

Attached is an Accurate Translation of the certified copy of German Patent Application No. 102 55 866.3. Per the Examiner's request, Applicant has included a Statement of Verification with the translation.

Upon review of the originally filed translation, it appears that translation errors may have been made. Any errors in the original translation were not made with deceptive intent. Any differences are not substantive and do not affect the fact that the presently claimed invention is supported by the priority filing.

Respectfully submitted,

<u>September 18, 2008</u>

Date

/Ira S. Matsil/

Ira S. Matsil Reg. No. 35,272

SLATER & MATSIL, L.L.P. 17950 Preston Road, Suite 1000 Dallas, TX 75252 972-732-1001